

## XA-10260 PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tetsuya KAMEI et al.

Appln. No.: 10/521,552 Group Art Unit: 2115

Filing Date: March 30, 2005

For: DATA PROCESSOR

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

Documents AE-AI and AQ-AR on the attached List were cited in the International Search Report (copy attached).

Document AA corresponds to Document AE.

Document AE was also cited in the specification, on page 3, and its relevance is indicated therein.

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C. 1751 Pinnacle Drive, Suite 500 McLean, Virginia 22102-3833 (703) 903-9000

June 7, 2005

Mitchell W. Shapiro

Reg. No. 31,568

FORM PTO-1449						Atty. Docket No. Appln. No.			No.	
INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT						XA-10260		10/52	10/521,552	
						Applicant				
						Tetsuya KAMEI et al.				
						Filing Date Group				
						March 20, 2005		2115		
			U.S.	PATI	ENT DO	CUMENTS	*	<del>_</del>		
Examiner Initial		Document Number	Date	Date		Name		Sub- class	Filing Date	
	AA	6,389,527	05/14/	5/14/02		Raam et al.		38		
	AB									
	AC					-				
	AD									
FOREIGN PATENT DOCUMENTS										
Examiner Initial		Document Number	Date		Country Class Sub- Translation class				Translation	
	AE	2000-231550	08/22/00	Japan No						
	AF	10-63502	03/06/98	Jap	an				Abstract	
	AG	4-195448	07/15/92	Jap	an	.,,			Abstract	
	AH	0 952 428	10/2799	Eur	ope			-		
	AI	2000-231549	08/22/00	Jap	an				Abstract	
	AJ								,, ,,	
	AK									
	AL									
	AM						-			
	AN									
	AO									
	AP									
	OTHER (including author, title, date, pertinent pages, etc.)									
	AQ M. ISHIKAWA et al., "A Methodology of Low Power On-Chip Memory Access in Embedded Microprocessor for Mobile Applications," The Institute of Electronics, Information and Communication Engineers, Vol. 102, No. 234									
		(ICD2002 35-45), July 18, 2002, pp. 1-6.								
AR SH7750 Programming Manual, Hitachi, Ltd., published April 19 9, 10-128, and 10-130.									pages 4-	
						Considered				
										EXAMINER:
		e through citation			tormance	e and not conside	red. I	nclude c	opy of this	